

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

J1060 U.S. PTO
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11/07/01

APPL NUM 10040002	FILING DATE 11/07/2001	CLASS 324	SUBCLASS 5	GAU 2858	EXAMINER Le
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**APPLICANTS: Lambert Donald; Redden John;

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**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO P JW151
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Electrical conduction array on the bottom side of a tester thermal head		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED	Assistant Examiner	CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwa. Figs. Drwa. Print Figs.	